Agenda CMG Canada Seminar: *Apr 15/16st 2008 (Toronto)*

TIME: **8:30a.m. - 5:00 p.m.**

NEW LOCATION: C'est What 67 Front Street East
`http://www.cestwhat.com/map.asp`

Sponsored in Part by CA Inc.

ATTIRE: Business attire or Business casual (hard-soled shoes, slacks, collared shirt)

If this program is not of interest to you, please pass it on to the appropriate group within your organization ... or have them visit

CMG Canada at

http://regions.cmg.org/regions/cacmg//index.html

Tuesday Apr 15th, 2008

8:30 AM ***Continental Breakfast***

9:00 AM **Welcome and Disscussion of Membership**

9:15 AM **Z10**
Tom Russell – IBM

This presentation will discuss the recently announced IBM System z10. The new hardware features available with this machine will be discussed. Infiniband coupling links, new channels, new instructions and a new chip design set the new standard for System z performance and availability.

Tom Russell is a Systems Architect in IBM Canada. He has more than 30 years of experience in IBM, supporting MVS, OS/390 and now z/OS. While working on assignment at the ITSO Poughkeepsie he wrote numerous books on Parallel Sysplex implementation and performance, continuous availability, and the Workload Manager. His areas of expertise include on-line systems design, continuous availability, hardware and software performance, and Parallel Sysplex implementation. Tom holds a degree in Mechanical Engineering from the University of Waterloo.

10:30 AM **Coffee**

10:45 AM Besides The Processor...
Gord Neill – IBM

Along with the IBM System z10 processor, there were a number of other complementary announcements. This session will be a potpourri of those features and functions, such as Capacity Provisioning Manager, Basic HyperSwap and z/OS V10 Preview to name a few, some of which require the new technology offered with System z10".

Gord Neill is a Consulting I/T Specialist with IBM Canada. He has over 30 years experience with IBM in the Large Systems area, and is the Technical focal point for System z processors in Canada. He is also the Subject Matter Expert for Systems Assurance Reviews, a mandatory review for the installation of all zSeries and System z processors. For the past 20 years Gord has specialized in the Capacity Planning and Performance area. Gord holds a BSc degree in Mathematics and Computer Science from York University.

12:00 PM Lunch (on your own)

1:00 PM Regulatory Compliance and the IBM Mainframe: Key Requirements
Reg Harbeck – CA

If you want to know how profound an impact Sarbanes-Oxley (SOX) has had on the American consciousness, all you have to do is look at the fact that, three times since it was introduced, the team that won the World Series was named SOX! Add to that Canada's version (aka "CSOX" or Bill 198) along with our PIPEDA and all the other regulations that impact businesses in North America, and it can seem like a mind-bending effort to keep up. There must be a better way to go about this from an IT perspective... right? Come to this session and decide for yourself!

Reg Harbeck received his Bachelor's Degree in Computer Science and has since worked with operating systems, networks, security and applications on mainframes, UNIX, Linux, Windows and other platforms. Reg has been with CA for over ten years, during which time he has met with and presented to IT Management and technical audiences in Europe, the Middle East and many locations across North America, including at SHARE, Gartner, IBM zSeries, CMG Canada and CA World user conferences. Reg is the published author of a number of whitepapers and articles, and also contributes to CA's mainframe blog at http://community.ca.com/members/Reg-Harbeck.aspx.

2:15 PM Adjusting RMPTTOM to Reduce SRM Overhead
Kevin Martin - McKesson

Last December Kevin Martin modified RMPTTOM to invoke SRM (System Resource Manager) less frequently and reduce system overhead. Kevin will summarize some discussions from the MXG list server about this topic, explain APAR OA18452 which adjusts the RMPTTOM default for some fast processors, and explain how he attempted to quantify the reduction in CPU even though SRM activity is not captured. This is intended to be an introductory level presentation about a component in the z/OS operating system.

Kevin Martin has more than 25 years experience in systems programming and performance management. He graduated from the University of Waterloo with a Bachelor of Mathematics degree. He has worked for several Canadian companies including the Bank of Montreal and American Express. For the past 10 years Kevin Martin has been working for McKesson in Dubuque, Iowa.

2:45 PM Coffee

3:00 PM On the Importance of I/O Parallelism and I/O Priority structures in z/OS Environments.

Anthony G. Mungal - EMC Corporation

This presentation details the necessity for I/O parallelism given the increasing I/O appetite of today's processors, and complimentary technologies such as high speed FICON, Extended Address Volumes (Large Volume support), multi-level caching and priority structures.
It traces the progression of Parallel Access Volume (PAV) technology from infancy to HyperPAV, and discusses the interaction of the HyperPAV feature within the context of the ever-expanding logical volume sizes, current I/O priority mechanisms, Symmetrix Priority Controls (SPC) and I/O Priority Control (IORP) and highlights the derived benefits to z/OS's Workload Manager (WLM) in achieving specified workload velocities and goals.

Anthony G. Mungal is a Consulting Corporate Systems Engineer with the EMC Corporation in Massachussetts. He lives, and is based, out of Boca Raton, Florida. He has been with EMC since July 1993 in various positions including: Consulting Systems Engineer, SE Consultant and Senior Product Manager. He is a 31 year veteran of the IT industry and has held positions such as: Product Manager, Large Systems Account Specialist, Regional Systems Engineer, Regional Product Specialist, Systems Engineering Management, Product Management, Systems Programmer, Business Systems Analyst, and Senior Programmer.

He is a graduate of the University of Toronto with honours in both Mathematics and Computer Science. He has participated on many IT discussion panels, authored and published numerous papers on Processor Performance, Memory Management, I/O Subsystems Configuration & Performance, Storage Management, IT Infrastructure and Architecture Design & Implementation, and other related topics which he has presented at forums such as Computer Measurement Group (National and Regional meetings), CMG International meetings (Australia, South Africa & the UK), SHARE, GUIDE and an assortment of other local IT related user groups. He is an active member of both the Association for Computing Machinery (ACM), and the Institute for Electronic and Electrical Engineers (IEEE).

4:15 PM Adjourn

CMG Canada Members' Networking Session

Wednesday Apr 16th, 2008

8:30 AM	***Continental Breakfast***

9:00 AM **Welcome**

9:05 AM **Seeing it All at Once with Barry**
Mario Jauvin – MFJ Associates

Improving data visualization paradigms for performance management is an orphaned area of tool development. Tool vendors avoid investing in development if they see no demand, while capacity planners and performance analysts do not demand what they have not conceived. We attempt to cut this Gordian knot with 'Barry'; a 3D performance visualization suite based on barycentric coordinates. Potentially thousands of active processors, servers, network segments or applications can be viewed as a moving cloutd of points that produces easily comprehended visual patterns due to correlations in the workload dynamics. Barry provides an optimal impedance match between the measured computer system and the cognitive computer system (your brain).

Mario Jauvin has over 25 years of working experience in the computer science field. He joined Nortel Networks information technology in 1984. He was a member of the Information Technology division for 18 years spending his last 5-6 years in team leading and management roles in the network management field. He was one of the original architects of Nortel Networks' state of the art performance metrics system (called WPMS) at a time when no commercial products were available for the UNIX environment. He since founded his consulting firm MFJ Associates with leading engineers in the field of performance management, capacity planning, software development and business continuity.

10:20 AM	**Coffee**

10:35 AM **THE MINIMUM DAILY ADULT**
The Right Metrics and the Wrong Metrics
Denise P. Kalm – CA Inc.

In capacity planning and performance analysis, we are inundated with metrics that purport to measure performance, but how we display them and understand them is what matters. Many metrics we take for granted are actually not that useful, and yet, permeate our world. This paper is intended to help you understand why the same-old, same-old metrics aren't good enough, and what works better.

"If you want to inspire confidence, give plenty of statistics. It does not matter that they should be accurate, or even intelligible, as long as there is enough of them. "
~ Lewis Carroll

Denise Kalm is a Senior Product Marketing Director at CA. She has 30 years experience in IT including application programming, enterprise systems management and performance/capacity planning at Pacific

Telephone and Bank of America. Prior to joining CA, Denise spent over five years at an enterprise management solutions software company focusing on Enterprise Performance Assurance. She is also a regional officer of CMG, has held many volunteer positions within that organization and is a frequent contributing author.

11:50 PM Lunch (on your own)

1:00 PM Solaris Virtualization
Scott Wardley – Royal Bank of Canada

Capacity Planning for Solaris Containers: Server virtualization is being deployed in RBC as the standard deployment method for SUN Solaris servers. Proven containment of IT costs and a method of easing Capacity requirements has allowed RBC to be both proactive in provisioning and reacting to market needs. This presentation will show the current Sun Solaris 10 virtualization technology, how it's being used in RBC and the opportunities it creates to easy pressure on Infrastructure teams.

Scott Wardley is a Resource Manager with Royal Bank of Canada and has worked on Capacity Planning for Windows, Solaris, and Linux with a focus on Capital Markets applications for the last 4 years. Scott's experience also includes 16 years as a SAS contractor programmer and has implemented systems for CIBC, Bell Canada, IBM, Ontario Hydro, Toyota and JP Morgan. His current focus is on virtualization technologies including Solaris Containers and Linux on VMware/IFL's and the financing of these environments.

2:00 PM Open Performance Security Architecture Past and Future
Kellman Meghu – Checkpoint

This session will explore the challenges of security performance, detailing where the impact to your network is greatest. An understanding of existing technologies for acceleration of security leads into a discussion of future plans for dealing with higher demand applications and security risks, including an overview of a new technology from Check Point Software, called CoreXL, that leverages new developments in open servers to accelerate security inspection far beyond today's standards.

Kellman Meghu is Manager of Security Engineers in Canada for Check Point Software Technologies Inc. His background includes 10 years of experience studying attacks, protection of deployed applications and network-based security. Since 1996 Mr. Meghu has been involved with consultation on various network security strategies to protect ISP's in Southern Ontario as well as security audits and security infrastructures across Canada and the Eastern United States. Prior to joining Check Point Mr. Meghu held Network and Security Engineering roles with Alcatel, Electronic Data Systems (EDS) and his own consulting organization. Presentations include private corporate focused events, as well as public events such as, SMB World 2005, Canadian Computer Measurements Group Canada, Hurricane Labs Conference (US), and Hackerfest 2005 (US).

3:15 PM Coffee

3:30 PM **Don's Diatribe VII**
Don Melton – Vatic Technologies

As part of my job as a consultant I try to know a little bit about many things and a lot about a few things. This presentation represents an accumulation of the former. It will identify some of what I believe are the most significant recent technology changes in the IT industry and elicit audience comments and discussion on them. Past presentations have included discussions around such diverse topics as: Privacy Legislation, CPU Architectures, Internet Futures, and many others. This session will be highly interactive, so bring your own ideas, comments, and gripes.

Don Melton received his Bachelor's degree in Engineering (Engineering Science option) from the University of Toronto in 1979. He is an active member and speaker at several local user groups (CMG Canada, NaSPA Ontario, COUG), a past presenter at SHARE, and a member of both the ACM and IEEE.

His career in the IT industry has spanned many fields including the IT service provider, education, transportation, financial, and small business sectors. He has also filled many diverse roles; working at various times as a systems programmer, capacity planner, performance analyst, IT manager, and enterprise architect. Since 1997 Don has been providing IT consulting services (through Vatic Technologies) to guide businesses in developing an IT vision and in their selection and use of IT solutions to deliver to that vision.

4:30 PM **Adjourn**

Dates to Remember

CMG International Www.cmg.org	Dec 7-12, 2008, Las Vegas		
CIPS BCM SIG www.cipstoronto.ca	Apr 17, May 15, 2008		